

**Amendments to the Claims:**

Claims 1-19 (Canceled).

20. (New) An access method for an apparatus to gain access to a specific area assigned to an area number of a memory device, the method comprising the steps of:

the apparatus transmitting designation information including the area number, an access start offset in the specific area associated with the area number and a size of data to access and designating an access area of the memory device, to the memory device;

the apparatus transmitting verification information generated based on the designation information, together with a processing command for the access area;

the memory device verifying the verification information using the designation information; and

the memory device executing the processing command when the verification succeeds.

21. (New) An access method for an apparatus to gain access to a specific area assigned to an area number of a memory device, the method comprising the steps of:

the apparatus sharing a verification key with the memory device;

the apparatus transmitting designation information including the area number, an access start offset in the specific area associated with the area number and a size of data to access and designating an access area of the memory device, to the memory device;

the apparatus transmitting verification data obtained by encrypting the designation information using the verification key, together with a processing command for the access area;

the memory device verifying the verification data using the designation information and the verification key; and

the memory device executing the processing command when the verification succeeds.

22. (New) An access method for an apparatus to gain access to a specific area assigned to an area number of a memory device, the method comprising the steps of:

the apparatus sharing a verification key with the memory device;

the apparatus requesting transmission of random numbers to the memory device;

the memory device transmitting random numbers to the apparatus;

the apparatus transmitting designation information including the area number, an access start offset in the specific area associated with the area number and a size of data to access and designating an access area of the memory device, to the memory device;

the apparatus transmitting verification data obtained by encrypting verification information including the random numbers and the designation information, together with a processing command for the access area;

the memory device verifying the verification data using the random numbers, the designation information and the verification key; and

the memory device executing the processing command when the verification succeeds.

23. (New) A memory device whose data of a specific area assigned to an area number is read and written by an apparatus, the memory device comprising:

a processing command receiver that receives designation information including the area number, an access start offset in the specific area associated with the area number and a size of data to access and designating an area to access and receives verification information generated based on the designation information and a processing command including a command for read or write;

a designation information verifier that performs verification processing on the verification information using the designation information;

a storage area that stores data;

a storage area access section that performs read or write from/in a designated area of the storage area according to the command for processing, when the verification processing succeeds;

a data transmitter that transmits data read by the storage area access section to the apparatus; and

a data receiver that receives data to write from the apparatus.

24. (New) The memory device according to claim 23, wherein the designation information verifier generates random numbers in response to a request by the apparatus, holds the random numbers and transmits the random numbers to the apparatus.

25. (New) The memory device according to claim 24, wherein the designation information verifier performs the verification processing using the verification information and a verification key.

26. (New) The memory device according to claim 25, further comprising:

a verification key sharing section that shares the verification key with the apparatus.

27. (New) An information apparatus that reads and writes data of a specific area assigned to an area number from/in a memory device, the information apparatus comprising:

a designation information determiner that determines designation information including an access start offset for reading and writing data from/in the specific area, a size of data for performing read and write and the area number and designating the access area;

a verification information generator that performs processing for generating verification information based on the designation information;

a processing command transmitter that transmits the designation information to the memory device and separately transmits the verification information and a processing command for reading or writing data;

a data transmitter that transmits data to the memory device when the processing command is write; and

a data receiver that receives data from the memory device when the processing command is read.

28. (New) The information apparatus according to claim 27, wherein the detection information verifier requests transmission of random numbers to the memory device and receives the random numbers from the memory device.

29. (New) The information apparatus according to claim 28, wherein the verification information generator performs the processing for generating the verification information using the designation information and a verification key.

30. (New) The information apparatus according to claim 29, further comprising:  
a verification key sharing section that shares the verification key with the memory device.

31. (New) An access method for an apparatus to gain access to a specific area assigned to an area number of a memory device, the method comprising:

the apparatus transmitting designation information including the area number, an access start offset in the specific area associated with the area number and a size of data to access and designating an access area of the memory device, to the memory device;

the apparatus transmitting verification data obtained by encrypting the designation information using the verification key, together with a processing command for the access area to the memory device;

the memory device verifying the verification data using the designation information and the verification key; and

the memory device executing the processing command when the verification succeeds.

32. (New) An access method for an apparatus to gain access to a specific area assigned to an area number of a memory device, the method comprising the steps of:

the apparatus requesting transmission of random numbers to the memory device;

the memory device transmitting random numbers to the apparatus;

the apparatus transmitting designation information including the area number, an access start offset in the specific area associated with the area number and a size of data to access and designating an access area of the memory device, to the memory device;

the apparatus transmitting verification data obtained by encrypting the designation information using a verification key, together with a processing command for the access area to the memory device;

the memory device verifying the verification data using the random numbers, the designation information and the verification key; and

the memory device executing the processing command when the verification succeeds.